

PATENT
450100-03853

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

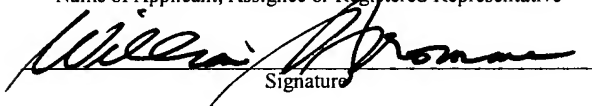
Applicant(s) : Masaki ENDO Notice of Allowance
Serial No. : 10/084,789 Dated: 11/19/2003
Confirmation No. 6221
For : DELAY LOCK LOOP CIRCUIT, VARIABLE DELAY CIRCUIT,
AND RECORDING SIGNAL COMPENSATING CIRCUIT
Filed : February 25, 2002
Examiner : Tuan Thieu LAM
Art Unit : 2816

745 Fifth Avenue
New York, New York 10151

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Box 1450, Alexandria, VA 22313-1450, on February 3, 2004

William S. Frommer, Reg. No. 25,506

Name of Applicant, Assignee or Registered Representative



Signature

February 3, 2004

Date of Signature

RESPONSE TO EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

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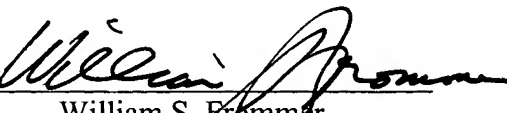
Sir:

This is in response to the Examiner's Statement of Reasons for Allowance, which
accompanied the Notice of Allowance mailed November 19, 2003. To the extent the Examiner's
Statement of Reasons for Allowance states, implies or is construed to mean that the claims are
allowable over the prior art of record because the Examiner believes the claims should be

interpreted to include one or more features or limitations not recited therein, Applicant's attorney disagrees with such an interpretation. Moreover, it is Applicant's contention that there is no particular limitation in the allowed claims that is more critical than any other. The issuance of the Examiner's Statement of Reasons for Allowance should not be construed as a surrender by Applicant of any subject matter. It is the intent of Applicant, by his attorney, to construe the allowed claims so as to cover the invention disclosed in the instant application and all equivalents to which the claimed invention is entitled.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP
Attorneys for Applicant

By 
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